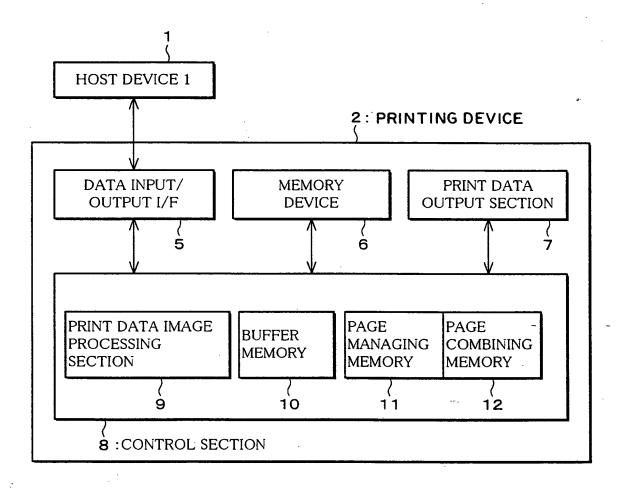


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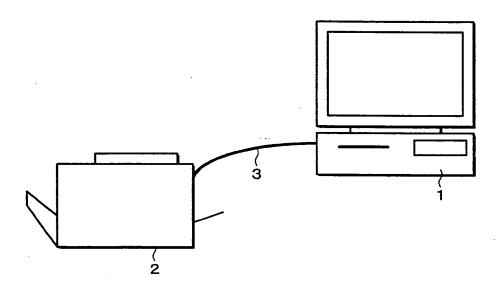
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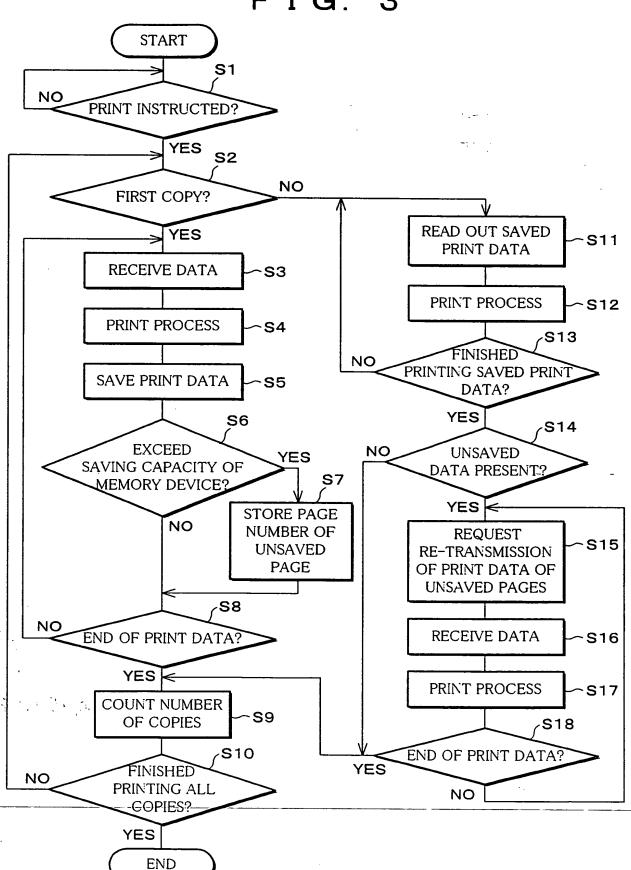
FIG. 2





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F I G. 3





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# F I G. 4

	RECEIVED PRINT DATA 13	PRINT DATA 14 OF MEMORY DEVICE 6	OUTPUT PRINT DATA 15
< <first copy="">&gt;</first>		·	
STATE 1			
STATE 2	1		1
STATE 3	2	1 2	2
STATE 4	K-1	1 2 ····· K-2 K-1	K-1
STATE 5	K	1 2 ····· K-2 K-1	К
STATE 6	K+1	1 2 ····· K-2 K-1	K+1
STATE 7	N	1 2 ····· K-2 K-1	N
< <second and="" s<="" td=""><td>SUBSEQUENT COP</td><td>IES&gt;&gt;</td><td></td></second>	SUBSEQUENT COP	IES>>	
STATE 8		1 2 ····· K-2 K-1	1
STATE 9		1 2 K-2 K-1	2
STATE 10		1 2 ······ K-2 K-1	K-1



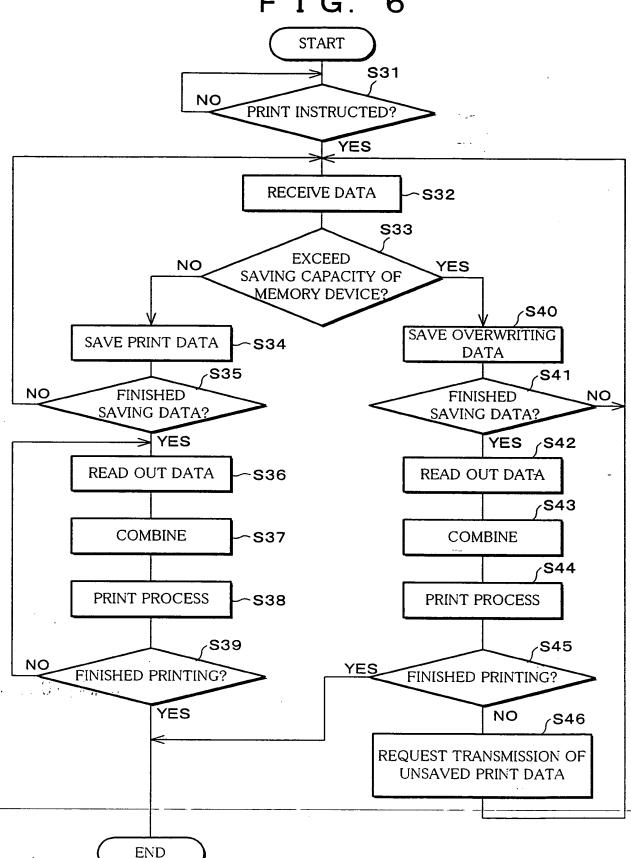
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# F I G. 5

	RECEIVED PRINT DATA 13	PRINT DATA 14 OF MEMORY DEVICE	OUTPUT PRINT DATA 15
< <first copy="">&gt;</first>			
STATE 1			
STATE 2	1	1	1
STATE 3	2	1 2	2
STATE 4	К	1 2 ····· K-1 K	К
STATE 5	K+1	K+1 2 K-I K	K+1
STATE 6	N	M M+1 N-1 N	N
< <second and="" s<="" td=""><td>UBSEQUENT COP</td><td>YIES&gt;&gt;</td><td></td></second>	UBSEQUENT COP	YIES>>	
STATE 7	1	M M+1 N-1 N	1
STATE 8	2	M M+1 · · · · · N-1 N	2
STATE 9	M-1	M M+1 · · · · N-1 N	M-1
STATE 10		M M+1 · · · · · N-1 N	M
STATE 11		M M+1 N-1 N	N



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## F I G. 7

RECEIVED PRINT DATA 13

PRINT DATA 14 OF MEMORY DEVICE 6

OUTPUT PRINT DATA 15

STATE 1												٠
STATE 2	1	1								)		
STATE 3	2	1	2									
STATE 4	K	1	2	•••	•••	χ	•••	K-1	K			• <del>-</del>
STATE 5	K+1	1	2	•••	•••	K+1	•••	K-1	K			
STATE 6	N	1	2		•••	М	•••	N-1	N		2	N-1
	<del></del>				, c	χ		•			1	N



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	RECEIVED PRINT DATA	13			IT DA MEMC ICE		1	٠.	DATA	UT PRII 15	NT
STATE 1											
STATE 2	1	1									
STATE 3	2	1	2								
STATE 4	2K	1	2	•••	К	K+1		••••	2K		
STATE 5	2K+1	1	2	•••	К	K+2	••••	· 2F	2K+1		
STATE 6	N	1	2	•••	К	N-K+	1	· · N-	1 N	<del></del>	
STATE 7		1	2	•••	К	N-K+	1	· N-	1 N		
											N
STATE 8	K+1 K+2	2	•••	K	K+1	K÷2	N-K+1	• • • • •	N-1	2	N-1
		. <b>-</b>								3	N-2
STATE 9	•	K-1	K	K+1		••••	• •	N-K+1	N-K+2	K-1	N-K+2
			<del>-</del>		•			·	<del></del>	К	N-K+1



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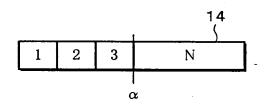
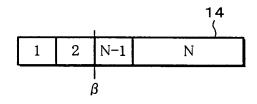
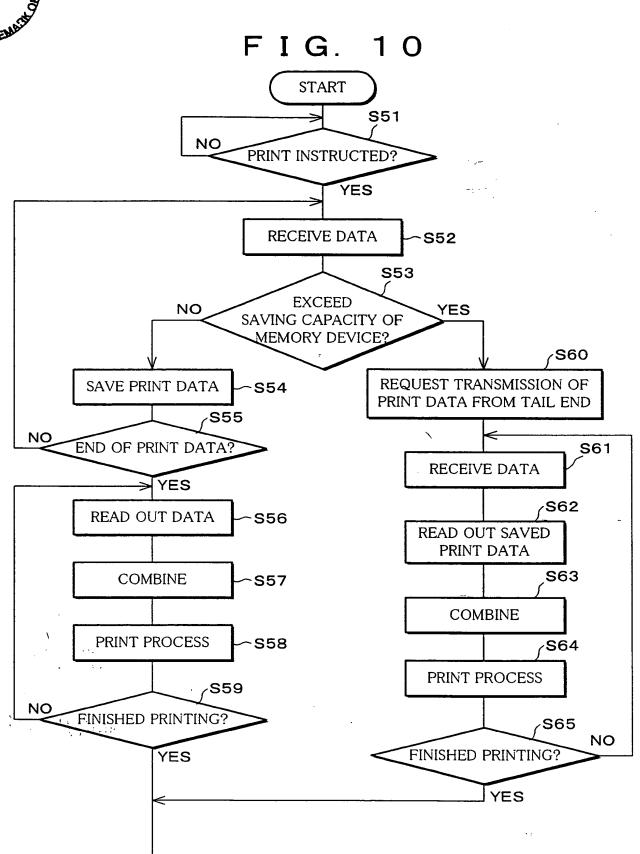


FIG. 9 (a)





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**END** 



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	RECEIVED PRINT DATA 13		OUTPUT PRINT OATA 15
STATE 1		-:-	·
STATE 2	1	1	
STATE 3	2	1 2	
STATE 4	K-1	1 2 ····· K-1	
STATE 5	К	1 2 ····· K-1 K	
STATE 6	N N-1	1 2 ····· K-1 K	2 N-1
			1 N
STATE 7		M M+1	M+1 M+2
			M M+3



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RECEIVED PRINT DATA 13 PRINT DATA 14 OF MEMORY DEVICE

OUTPUT PRINT DATA 15

STATE 1

1 2 ····· N-1 N

2 N-1 1 N

STATE 2

K K-1

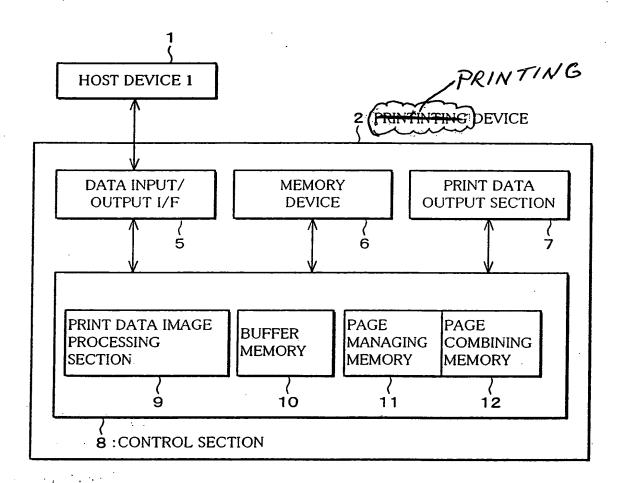
3,N-2 4,N-3 · · · · K-1 K



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	RECEIVED PRINT DATA 13	PRINT DATA 14 OF MEMORY DEVICE	OUTPUT PRINT DATA 15
< <first copy="">&gt;</first>		DEVICE	
STATE 1			
STATE 2	1	1	1
STATE 3	2	1 2	2
STATE 4	К	1 2 ····· K-1 K	K
STATE 5	K+1	K+1 2 12 K-1	K+1
STATE 6	N	M M+1 N-1 N	N
< <second and="" st<="" td=""><td>JBSEQUENT COP</td><td>PIES&gt;&gt;</td><td></td></second>	JBSEQUENT COP	PIES>>	
STATE 7	1	M M+1 N-1 N	1
STATE 8	2	M M+1 · · · · · N-1 N	2
STATE 9	M-1	M M+1 · · · · N-1 N	M-1
STATE 10		M M+1 · · · · · N-1 N	М
STATE 11		M M+1 · · · · · N-1 N	N